



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent application of:

Alexander Lidow et al.

Serial No.: 07/653,017

Filed: February 8, 1991

Examiner: S. Loke

Group Art Unit: 2508

Notice of Allowance: February 24, 1992 Batch No.: A42

March 20, 1992

For: PLURAL POLYGON SOURCE PATTERN FOR MOSFET

Hon. Commissioner of Patents and Trademarks

Washington, DC 20231

Attention: Official Draftsman

## FORMAL DRAWINGS

Sir:

Responsive to Paper No. 9/B attached to the Notice of Allowance dated February 24, 1992, enclosed herewith please find 3 sheets of formal drawings containing Figures 1 through 5 for the above-identified application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington,

D.C. 20231, on \_\_\_ March 20, 1992

Samuel H. Weiner

Name of applicant, assignee or Registered Representative

> Signáture March 20, 1992

Date of Signature

Respectfully submitted,

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